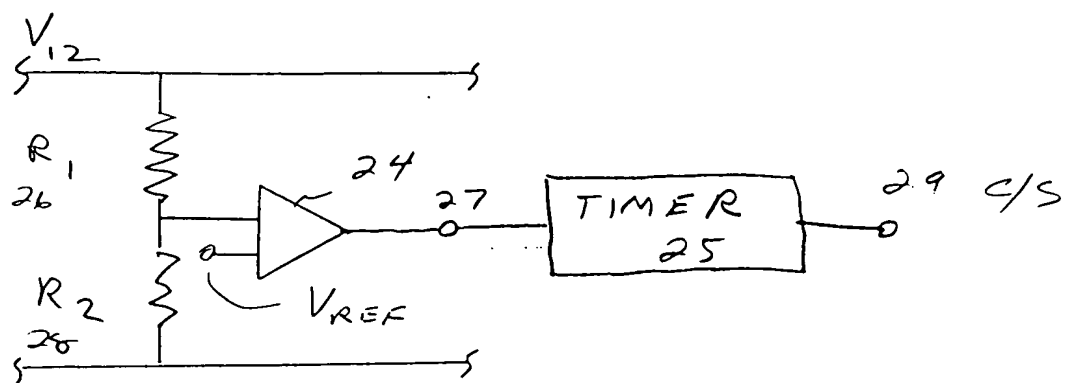


FIG. 1



16.2

[illegible]

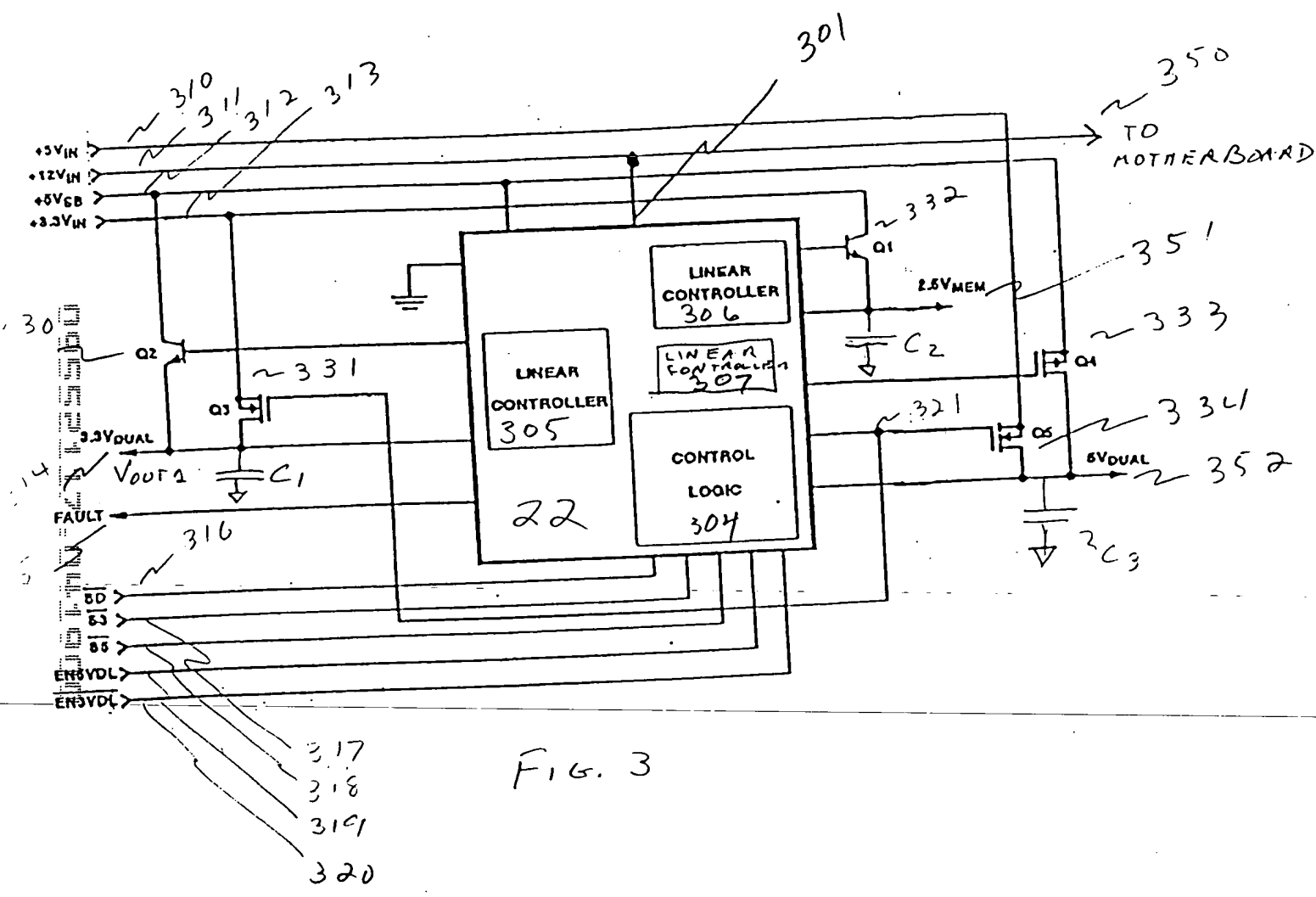


FIG. 3

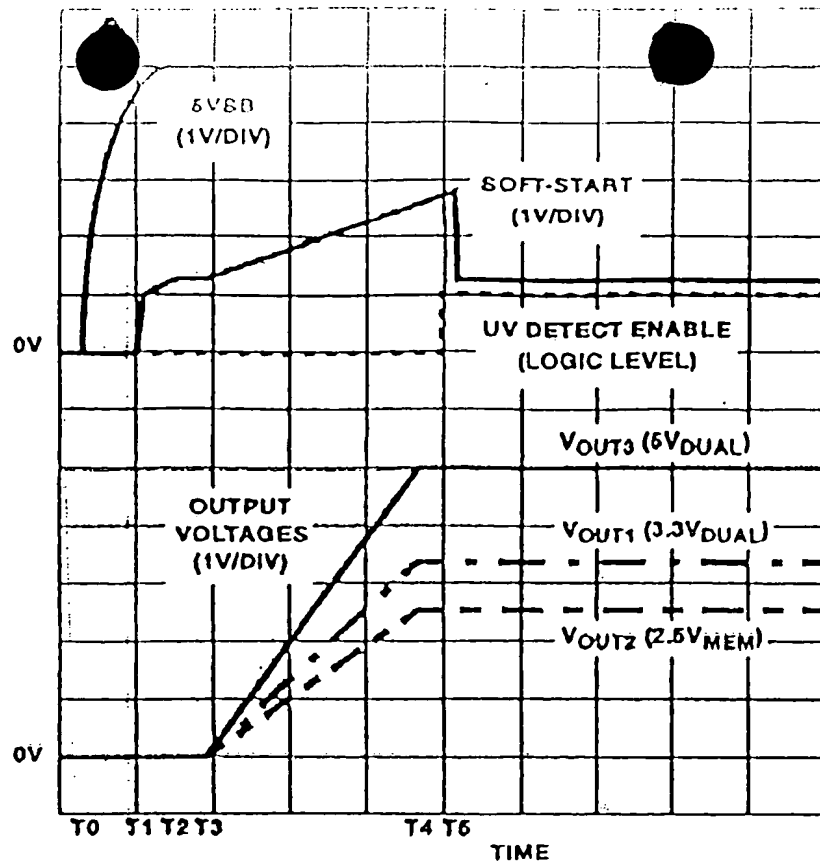


FIGURE 4 SOFT-START INTERVAL IN A SLEEP STATE (ALL OUTPUTS ENABLED)

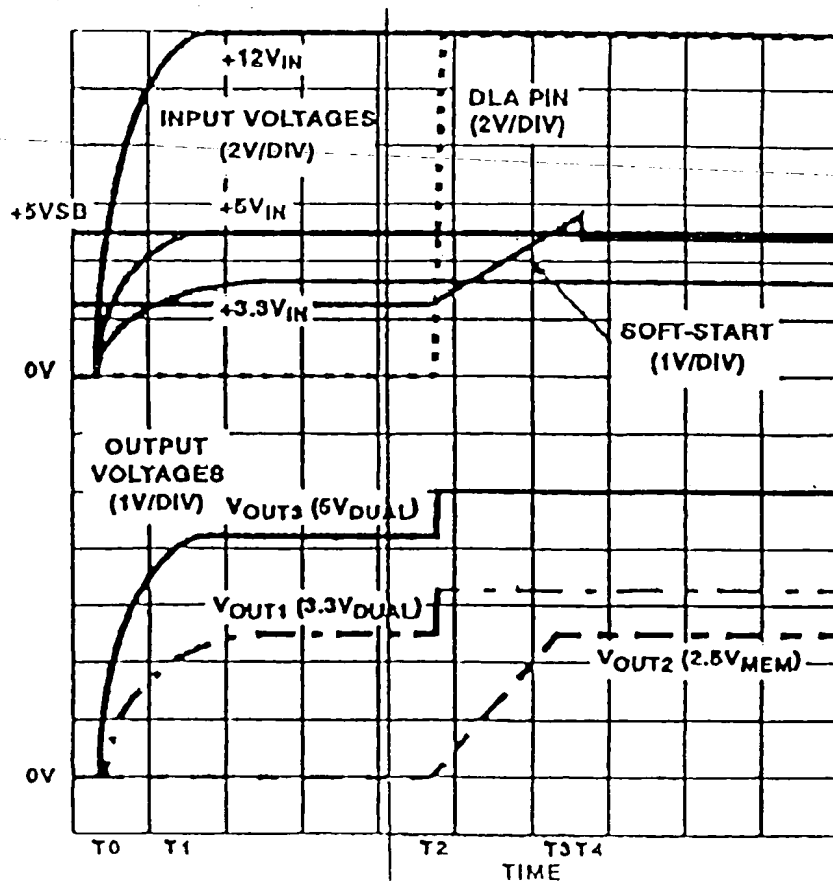


FIGURE 5 SOFT-START INTERVAL IN AN ACTIVE STATE